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ABSTRACT

We use patent citation networks to study the dynamics of technical change in Ethernet, a standard for data communication. We first consider the evolution of the broad technological system of Local Area Networks (LANs) and we identify the most significant inventions within the specific subfield of Ethernet. Then we analyse the structure of connectivity of patent citation networks to reconstruct the main technological trajectories in this subfield. Our results suggest that these technological trajectories are characterized by the presence of a number of interconnected technological environments which cluster following an engineering logic. These clusters include the most significant patents related to 'milestones inventions' in the evolution of the LAN technology. Thus, our analysis of the structure of connectivity in patent citation networks seems appropriate to capture both the cumulateness associated to the development of a specific technology and the significant discontinuities that punctuate the trajectory, as well as the technological interrelatedness characterising large technical systems.

Key Words: Technical systems, Technological trajectories, Patents, Network analysis, Data Communications

JEL: O30; O33; L96;

1. Introduction

An extensive literature exists on innovation in 'large technological systems' (Bunn and David, 1988). This literature outlines how the dynamics of technical change is affected by the role played by network effects (Katz and Shapiro, 1994), technological interrelatedness (Antonelli, 1993), and the need for compatibility standards (David and Greenstein, 1990). In particular, it has been pointed out that a proper understanding of the evolution of these systems requires an appreciation of the technical bottlenecks arising from the need of integrating different interacting components. In large technical systems innovation and change at the component level create unbalances between the different parts of the system. These unbalances act as 'focussing devices' (Rosenberg, 1976) or 'heuristics' (Dosi, 1982) for inventing activities.

This paper examines the patterns of technical change in large technical systems as captured by patent citations networks. One of the most common approaches for evaluating the importance of (patented) innovations is to 'weigh' them by using patent citations. Indeed, several studies have generally found that patent citations provide a reasonable 'proxy' for their technological significance, as they generally appear to be highly correlated with other measures of the value of innovations such as the assessments by technology experts (Jaffe and Trajtenberg, 2002). In this paper we argue that, in the specific case of technical systems, this approach may have drawbacks. First, innovation in technical systems is '*distributed*' in the sense that it takes place at the level of individual components across the system. However, single components need to 'work together'. Thus changes in one specific component trigger changes in other parts of the system. Second, due to these systemic features, innovations tend to be *incremental* in

nature and generally occur around well established technical designs in order to ensure that adequate levels of compatibility are maintained (Antonelli, 1992: p. 12). In this context, an analysis based simply on citation counts may underestimate the significance of specific innovations. Third, innovations tend to occur *continuously*. Both the incremental nature of innovations and their systemic nature contribute to generate a rapid pace of technical change (Antonelli, 1993). Again, an analysis based on citation counts may fail to identify those concepts and principles that could act as ‘focussing devices’ for a sequence of inventive activities.

In this paper we consider the case of Ethernet, a standard for data communication. Data communication equipment comprises all the capital goods enabling the transmission of data among a defined set of computers (such as for example the Local Area Network of a university campus). From a technological point of view, Local Area Networks (LANs) present several points of interest. First, LANs represent the archetypical case of a large technical system in which components must conform to specific communication standards. Accordingly, the history of this technology, whose beginnings can be traced back to the early 1970s, has been punctuated by the emergence and demise successive communication standards (Ethernet, Token Ring, Fast Ethernet). Second, the technology is characterized by a relatively fast rate of innovation and by strong uncertainty related with the availability of several technological options.

Our analysis is based on the study of citation links among patents in data communication equipment granted by the US patent office. Our basic assumption is that patent citations represent an indicator of the prior knowledge underlying a specific

inventive step. Thus, by examining the structure of the patent citations networks in a technological field, one should be able to identify the main trajectories that have characterized its evolution. Our methodology for the analysis of the patent citation networks follows Hummon and Doreian (1989), who have suggested a number of criteria for the identification of the main paths of 'connectivity' in a network which can be understood as the fundamental technological trajectories (Dosi, 1982) in a given field (see Verspagen, 2007 and Mina *et al.*, 2007 for two recent contributions in this vein).¹

The analysis of the connectivity of patent citation networks has important implications for assessing the 'relevance' of innovations in large technical systems. By extracting the most significant sequences of patents, the structure of connectivity approach allows the identification of the key technological bottlenecks and focussing devices that have played a major role in the evolution of the system. Thus, technologically significant patents should belong to the 'main paths' of the citation networks and/or locate at particularly critical 'junctions' within those paths. A second merit of this approach is that, by reconstructing technological trajectories as sequences of patents, it opens the opportunity for a fruitful reconciliation between quantitative and qualitative insights in the study of large technical systems. Indeed, the approach allows to pin down a restricted number of patents, whose content can then be examined in detail. By reading the content of these patents, it is possible to reconstruct the heuristics governing inventive activities and go beyond the assessment of innovation patterns based only on patent counts.

¹ The original study of Hummon and Doreian (1989), deals with scientific research (they reconstruct the main path in the network of citations between scientific papers leading to the discovery of DNA).

The structure of the paper is as follows. Section 2 illustrates the use of indicators of connectivity structure for the analysis of patent citation networks. In Section 3 we provide a short history and overview of the evolution of the Ethernet standard based on a detailed inquiry of the engineering and industry literature. Section 4 describes the construction of our patent data-set and gives a preliminary descriptive analysis of its main properties. Section 5 presents the analysis of the patent citation networks using connectivity structure indicators. Section 6 concludes.

2. Mapping technological trajectories using patent citation networks

Patent citations have been frequently used to measure the ‘importance’ of a specific (patented) innovation. The idea behind this approach is very simple. If a patent is cited very frequently, this means that it contains a ‘piece of knowledge’ that forms the basis for several subsequent inventions. Hence, the patent in question ought to be regarded as ‘technologically’ important. Empirical studies have generally confirmed the existence of a positive and significant relationship between the number of citations received by a specific patent and other indicators of technological and economic importance (Jaffe and Trajtenberg, 2002).

By looking comprehensively at the evolution of patent citations in a specific technological field, one is typically confronted with a network of patent citations. An intuitive interpretation of these networks is that they can be understood as representing the relationships between the pieces of knowledge contained in the individual patents. Following this reasoning, it should be possible to trace ‘technological trajectories’ in the

sense of Dosi (1982) in the evolution of patent citation networks (see Verspagen, 2007 for a more extensive treatment).

Additionally, when we take this broader view and consider the network of patent citations in a certain technological field as providing a possible representation of the underlying dynamics of knowledge, it also appears that the traditional approach of measuring the technological importance of patents by looking at the number of their *direct* citations may be fraught with difficulties. As shown by von Wartburg *et al.* (2005), assessing the importance of patent using the number of *direct* citations received amounts to use 'centrality' measures for assessing the importance of nodes in a network. This approach may be clearly justified both on intuitive and empirical grounds. However, one may also suggest that this exercise ought to be integrated with a study of the whole 'connectivity structure' of the network in question. In other words, a proper assessment of the technological significance of a specific patent ought to be based *both* on direct citations *and* on a general characterization of the position of the patent in the overall structure of the network of patent citations.

Several approaches based on network theory may be used for this task. In this paper, we will follow the approach proposed by Hummon and Doreian (1989) which has been already adopted for the analysis of citation networks in different technological fields (Mina *et al.* 2007; Verspagen, 2007). Hummon and Doreian (1989) are interested in identifying what may be considered the main paths of a network which represent the relationship between pieces of knowledge. These main paths should be viewed as the 'main flows of ideas' characterizing the structure of the network in question.

Hummon and Doreian assign a weight to each citation link on the basis of its position in the overall structure of the network. The method is based on the examination of the different 'search paths' existing in the network. Search paths are sequences of links that connect the vertices of the network. The method is illustrated in Figure 1A. A more extensive and formal treatment is contained in Verspagen (2007). In the figure the vertices represent the patents and the edges represent the citations. The edges are oriented from the cited towards the citing patent, so that the arrow represents the direction of the knowledge from the cited to the citing patent. For example, in the figure, patent A is cited by patent C. Patent C in turn is cited by patent D.

[Figure 1 about here]

A search path is represented by the sequence A - C - D - F - H - J, which would indicate a flow of knowledge from patent A to patent J through several intermediate patents. The simplest indicator proposed by Hummon and Doreain is the so-called *search path link count* (SPLC) measure. The SPLC is calculated by considering all the possible search paths in the network, and counting how often an edge lies on such a search path.

Although Hummon and Doreain suggest to include even those search paths involving intermediate vertices as the origin or final destination, one may also use only the search paths from start points to endpoints. In this case, one obtains a value of SPLC of 4 for the edge A - C (paths $A \rightarrow E$, $A \rightarrow G$, $A \rightarrow J$ and $A \rightarrow I$), of 8 for the edge C - D (paths $A \rightarrow E$, $A \rightarrow G$, $A \rightarrow J$, $A \rightarrow I$, $B \rightarrow E$, $B \rightarrow G$, $B \rightarrow J$ and $B \rightarrow I$) and so on and so forth.

An alternative measure proposed by Hummon and Doreian is the *search path node pair* (SPNP) indicator. Consider the edge C - D. This edge connects 3 vertices (A, B and C) to its destination (i.e. vertex D). At the same time, it is responsible for connecting its origin (vertex C) to other 7 vertices (D, E, F, G, H, I, and J). We define the SPNP value for this edge as the product of these values ($3 \times 7 = 21$), because in total 21 distinct pairs of vertices are connected by it. As a result of this multiplication, the SPNP measure 'weights' edges in the middle of a path relatively more than those at the start or the end. The logic underlying both measures is that citation links that are responsible for connecting higher number of patents contain the most significant knowledge flows of the citation network.

Once the weights to the links of the network have been assigned using the SPLC or the SPNP procedures (in most of the cases the methodologies lead to similar results), Hummon and Doreian suggest a procedure for constructing the network of 'main paths'. This is represented by the network constructed by moving from each 'start point' towards an 'endpoint' of the network. In doing this, at each node, one must follow the link with the highest SPLC or SPNP value. In the case of a tie, both links are considered as part of the network of main paths. Figure 1B represents the edges of network of main paths for the original network of figure 1A, as marked by thicker lines. In this case, the construction of the network of main paths does not change if we use as weights of the edges the values of SPLC or SPNP and it involves the deletion of the edges D - E and F - G. In other words this heuristic procedure implements the idea of identifying the most

important knowledge flows in the citation network by deleting marginal or redundant edges.²

3. Data communications technology: a short history and overview

In this paper we apply the patent citation network approach to study the evolution of data communication technology. Given the broad scope of this technological field, we focus on a specific subfield related to technologies and equipment for LANs. Within this subfield we consider in detail the patents related to the Ethernet communication standard. In this section we provide the relevant background information to understand the development of Ethernet and to put it within the more general context of the evolution of technological systems such as LANs.

The simplest way to define a LAN is as a set of communication conduits connecting computers or other devices capable of sending and receiving digital information. These conduits may consist either of ordinary telephone wires or more sophisticated cabling systems. They may connect devices in different patterns which are referred to as topologies. LANs started to be developed in the early 1970s although experiments with computer networking had originated in the late 1950s and early 1960s.³ These early

² The implicit assumption is that the that significance of a citation link is adequately captured by its SPLC or SPNP weight.

³ The first data communication network was built in 1958 by the US Air Force as part of the Semi-Automated Ground Environment (SAGE) air-defence system. The first civilian application of a data network built around the principles of SAGE was realised in 1964 with the development of the Semi-Automatic Business Related Environment (SABRE) airline reservation system by American Airlines. One characteristic

LANs were highly heterogeneous and 'proprietary' systems, developed mainly for intra-company use. They provided stable and simple connections between a mainframe (IBM type) or minicomputers (DEC type) and 'dumb' terminals. The major growth in the LAN market was a direct consequence of the advent of microcomputers in the mid-1970s and PCs in the 1980s. While the advent of the microcomputer entailed the possibility to economically provide each user with an end station, the extension of peripherals services (mainly printing) to single users proved to be very expensive. Tying users together was perceived as a way to reduce the costs while extending the access to an increasing amount of users. This event spurred the diffusion of LANs.

From a technological viewpoint we can identify four phases in the evolution of LAN technology (Christensen *et al.*, 1995). Each phase is associated to the introduction of a particular type of component as well as to the dominance of a specific communication standard. The first phase, from mid-1970s to the mid-1980s, is characterized by the absence of an official communication standard and the presence of many proprietary solutions marketed by different manufacturers. The second phase, from the mid-1980s until the end of the decade, saw the introduction of equipment called 'hubs' and the official ratification of two communications standards (Ethernet and Token Ring). Hubs were introduced mainly to offer a unique point of concentration within offices in order

of both the SAGE and SABRE communication systems was that terminals were connected to the central mainframe through telephone lines. Given the characteristics of the medium, these networks operated at lower speed than the later LANs. Both their low speed and the fact that these networks were nation wide made these early application Wide Area Networks (WANs) rather than local networks (Hellige, 1994; von Burg, 2001: 56).

to rationalize the cabling system. The third phase, from the end of the 1980s until mid-1990s, witnessed the growth of the hub market and the establishment of Ethernet as the dominant standard for LANs. As more and more nodes were added to the LAN, congestion increased revealing the shortcomings of hubs in solving new bottlenecks. The introduction of another type of equipment called 'switch' and the emergence of Fast Ethernet as the high-speed upgrade of Ethernet characterized the fourth phase (Fontana and Nesta, 2006).

In the remaining parts of this section we examine in detail the Ethernet standard. The aim is to identify the main innovations that have marked its evolution. This type of analysis will provide a roadmap to guide the interpretation of the results of our empirical analysis.

3.1. The evolution of Ethernet

Together with the diffusion of microcomputers, the invention and commercialization of Ethernet represented a crucial event for the diffusion of LANs. Ethernet was invented in 1973 by Robert Metcalfe at the Xerox PARC research centre in Palo Alto as a part of a project undertaken by Xerox aimed at creating an 'architecture of information' in which personal computers instead of minicomputers, file servers, and laser printers could all be linked by a network. This project was part of a comprehensive strategy by Xerox aimed at extending its core business and embracing the computer technology. The strategy eventually culminated in the development of the Alto PC, later commercialised as Star workstation, in 1973. The establishment of the PARC laboratories, whose activity

resulted in the invention of many revolutionary technologies such as laser printers and file servers, was essential for achieving the aim (von Burg, 2001: 67-69).

The Xerox network responded to specific technical and economic requirements. First, it was able to transfer data at higher rates than could be achieved by phone wires over WANs because high speed was required to maximise the performance of laser printers. For this reason, Metcalfe decided to employ *coaxial cables* as transmission media as they could achieve a transmission rate of up to a thousand kilobits per second. Second, it was able to span hundreds of metres to connect all computers deployed throughout a large company campus. To achieve this goal a *bus topology*, potentially able to branch out in every direction at each point to accommodate additional computers, was chosen. Third, it was *cheap*. Therefore, Metcalfe devised ways of limiting the cost of connection to 5% of the cost of a computer (Metcalfe, 1994).

Moreover, the network invented by Metcalfe benefited from three basic developments in data communications which had been achieved in the past.⁴ The first development was *packet switching*. Packet switching, was a communication method which, by breaking a message into small units (*frames or packets*), allowed the message to be sent in a

⁴ These developments were among the major achievements of the ARPANET project. ARPANET was a computer network, built up at the end of the 1960s, to connect research centres engaged with the Defence Advanced Research Project Agency (DARPA) funded by the US Department of Defence. Originally only those centres working on military related fields were part of the network. Later on other centres, government agencies and eventually countries joined it opening the way to what later on would constitute the Internet (O'Neill, 1995; von Burg, 2001: 58)

discontinuous stream which did not require establishing and maintaining a direct and dedicated link between two nodes for the duration of the transmission as required by the *circuit switching* method applied in telephone networks. The second development was *layering*. Layering consisted of breaking up the communications process into several steps each performing a specific function in order to reduce the complexity of the whole transmission and enabling manufacturers to focus on the development of protocols within a single layer at a time instead of tackling the whole process at the level of the overall network.⁵ The third development was the definition of a *network interface* ('transceiver') to connect the computer to the medium of choice and accomplish the tasks of both taking the message from computers and formatting it in the appropriate manner in order to be transported over the LAN to other stations and receiving the messages from the network and reformatting them in a form which was understandable by the local computer.

One of the major problems of designing a network in a non-mainframe environment was to devise a way of allocating access to the channel across all the nodes. To regulate the sequence of accesses and control the duration of each transmission over the network Metcalfe's designed an 'access method' based on an algorithm which later became

⁵ The layer system was subsequently adopted by the International Standards Organisation (ISO) which developed a reference model, the Open System Interconnection (OSI) model, based on seven functional layers: physical layer, data link layer, network layer, transport layer, session layer, presentation layer and application layer. LAN technologies were mainly concerned with the first and the second layers therefore making data transmission completely independent from the definition of higher level protocols.

known as the Carrier Sense Multiple Access with Collision Detection (CSMA/CD) method.⁶

Metcalfe's ideas led to the implementation of an Ethernet based LAN in the PARC network in 1974 when, together with David Boggs, he developed and subsequently patented the first transceiver. At first, the outcome of the collaboration between Metcalfe and Boggs remained an isolated attempt.⁷ The implementation of Ethernet on a large scale became possible only with the important developments which took place in the 1980s. The first development was the increase in the speed of Ethernet from 2.94Mbps to 10Mbps which occurred after the formation of the DIX Alliance, involving DEC, Intel and Xerox. This alliance was set up in 1979 with the explicit goal of promoting Ethernet as an industry wide de-facto standard, making the technology available to anybody (von Burg, 2001: 102-107). The choice of joining efforts to promote Ethernet as an industry-

⁶ According to CSMA/CD, all stations on the network could achieve access to network communication capacity without specifying a priority or hierarchy to any of them. Stations requesting access to the network channel had to be able to check that the channel (or 'carrier') was idle before transmitting and defer transmission in the case a transmission signal was detected. However, when two stations found the channel idle and started to transmit simultaneously frames might collide on the channel. This access method was able to register collisions, notify the stations that this had occurred and reschedule the transmission very rapidly so that, following a collision, stations could choose a random delay in retransmission.

⁷ The first Ethernet network connected hundreds of nodes and had a speed of 2.94 Mbps which was useful for the PCs and the laser printers installed at the Xerox PARC (Metcalfe, 1994). However, it far exceeded the needs of most of LAN users at that time. Up until the mid 1980s, the memory of most of available PCs was less than 2Mbps. For these computers, simpler technologies seemed to be more appropriate and cost effective.

wide open standard, responded to specific needs and strategies of the three main proponents. These strategies are typical of competition in technical systems characterised by the presence of 'network effects' where the main goal is to reduce costs by widening the installed base of the system components. First, neither Xerox nor DEC saw the LAN technology as their core business. On the contrary, they thought that a wide acceptance of Ethernet would help them promote the diffusion of Ethernet 'complements' such as their computer hardware, the Alto based office system and the VAX system respectively. Second, both companies realised that the diffusion of Ethernet would ultimately depend on the availability of other Ethernet components such as transceivers. Promoting Ethernet as an industry wide 'open' standard could lead to the creation of independent suppliers of Ethernet components. Third, among the required components, chipsets were the most important. Intel thought that a strategy of open licensing could help achieve the volumes necessary to spread the high costs of manufacturing and designing them (von Burg, 2001: 102-107).

The second development was the starting of an official standardization process within the Institute of Electrical and Electronic Engineers (IEEE hereafter) eventually leading to the official (de-jure) standardization of Ethernet in 1985. Ethernet started to be considered for standard definition by the IEEE Project 802 group in February 1980. This effort overlapped with the attempts of the DIX Alliance to achieve de-facto market dominance.⁸

⁸ Indeed, the position of DEC, Intel and Xerox with respect to the IEEE initiative ranged from opposition to support conditional on the acceptance of their proposal as the main basis for the standard. Before official standardisation occurred, the DIX alliance published their own proprietary set of Ethernet specification

In 1981, 3Com, the Xerox spin-off funded by Metcalfe, shipped the early transceiver and controllers compatible with DEC minicomputers. In the same year, DEC in collaboration with AMD and Mostek started developing early integrated circuits for Ethernet transceivers. It was quickly followed by other manufacturers such as 3Com and Ungermann Bass which struck a collaboration agreement with Seeq Corp. and Fujitsu respectively. Further milestones were linked to variants to the original design. In August 1981, 3Com redesigned the transceiver and the Ethernet board to shrink size in an attempt to target the PC market. At about the same time a new version of Ethernet for thinner coaxial cables was developed.⁹ Finally the end of the decade witnessed the standardization and commercialization of a version of Ethernet (10Base-T) running over twisted pair copper cables.

The diffusion of Ethernet occurred in parallel with the development of another communication technology: Token Ring. Token Ring was based on the early work of David Farber who, in the early 1970s at University of California Irvine, had invented a network based on an access method which was different from the one underlying the functioning of Ethernet. In a Token Ring network, transmission was controlled by a bit

(known as the 'bluebook' v.1 and v.2). Its position, combined with pressures from other manufacturers sponsoring solutions based on different technologies such as Token Ring, contributed to delay the standardisation process and ended up undermining the efforts of creating a unique LAN standard (Sirbu and Hughes, 1986).

⁹ This version became known as 'thin Ethernet' (10Base-2) as opposed to 'thick Ethernet' (10Base-5) which was the original Ethernet standard.

pattern (the token) which was sent continuously and uni-directionally around the ring. Stations willing to transmit seized the token and passed it by with a destination address and data packets attached to it. When data were received, the token returned to the sender node which had to pass it around free before transmitting again thereby avoiding monopolizing the communication. Contrary to CSMA/CD this access method was deterministic because it enabled to calculate the time interval between successive appearances of the free token. In a Token Ring network, each node could act as a repeater of the signal so there were no limits on speed or distance as in the bus topology. Moreover, it could be deployed either in a ring or in a star topology as opposed to the bus topology of Ethernet. A ring topology gave Token Ring a competitive advantage over Ethernet whose bus topology was ideal for connecting a small number of stations within a building but could prove to be ineffective for connecting stations between buildings (Sirbu and Hughes, 1986; von Burg, 2001: 62-67).

Variants of the 'Farber ring' flourished during the 1970s such as the Cambridge Ring developed in 1974 or the 'star shaped ring' developed at MIT by Saltzer, Pogran and Clark. In parallel IBM had been experimenting with Token Ring over its own computer systems.¹⁰ At the beginning of the 1980s other manufacturers had started shipping Token Ring products targeting several kinds of environment. In 1981 Proteon, a start-up company had shipped a proprietary 10Mbps Token Ring to be used by universities and research institutes. LANs, mainly based on proprietary 'token based' topologies were

¹⁰ In the 1970s IBM were using the ring topology to link controllers to looped workstations in the IBM 3600 on-line computer system mainly adopted by banks (Abrahams, 1991: 2).

also diffused in factory environments as opposed to offices (Bresnahan and Chopra, 1990). In 1984, the Token Ring standard was first drafted by the IEEE.

Since most of its characteristics were already known, several manufacturers, beside IBM, were supporting Token Ring in the competition against Ethernet. Two elements were crucial in the evaluation of which standard to adopt. The first element was the kind of media supported. From this viewpoint Token Ring held a cost advantage since Ethernet required coaxial cable which was more expensive than the Shielded Twisted Pair (STP) wires chosen for Token Ring. The second element was the extension of the configuration. Again Token Ring had a cost advantage. Because of its bus topology, Ethernet usually required a higher (and more expensive) total length of cabling than the ring topology. One technical advantage of Ethernet over Token Ring was its higher speed (10Mbps vs. 4Mbps). The combination of a more expensive wiring system and a wider area to connect in most cases represented a disadvantage on the demand side for Ethernet compared to Token Ring. Nevertheless, the demand for LAN connections turned out to be relatively inelastic with respect to connection cost.¹¹ Moreover, costs were deemed to decrease quite fast as a consequence of technical change coupled with the strategy of open licensing *deliberately* pursued from the start by the DIX Alliance and culminated in the standardisation effort by the IEEE. Toward the end of the 1980s

¹¹ In 1989, a user survey by the journal Data Communication revealed that, although coaxial cables were more expensive than copper based ones, 90.4% of LAN users employed coaxial cables for their networks and 63.5% had a network which spanned between buildings. Moreover, for most of the users LAN functionality was the most important factor behind their LAN purchase. Price was decisive only for 6% of them (Data Communications, June 1989 Issue).

Ethernet was able to attract more support from manufacturers than Token Ring.¹² A mix of technological characteristics, standardisation strategy, and temporal advantage had given decisive momentum to Ethernet when compared to the competing alternative.

3.2. Further developments and the arrival of the Fast Ethernet standard

The standardization of both Ethernet and Token Ring accelerated network expansion and represented a turning point in the diffusion of LANs. From a technical viewpoint, expanding LANs and developments such as the integration of voice and video transmission with data transmission also brought an increase in the quantity of data and information that would be transmitted which increased congestion and reduced performance. The most straightforward way to reduce congestion was by increasing the aggregate LAN bandwidth by developing new standards. The definition, development and commercialization of new LAN standards, characterized the 1990s when, as the result of the efforts of manufacturers co-ordinated by official standardization bodies, three high speed standards for LANs were developed: Fast Ethernet, 100VG-AnyLAN and Asynchronous Transfer Mode (ATM).¹³

Fast Ethernet and 100VG-AnyLAN were both developed by the IEEE as a result of a long and complex process of standardization during which proponents could not find

¹² The number of manufacturers supporting Ethernet rose from 34 in 1985 to 99 in 1987 and topped 131 in 1989. In 1985, only 5 manufacturers were supplying Token Ring products in 1985 although their number rose to 53 and 77 in 1987 and 1989 respectively (Fontana, 1999).

¹³ A fourth standard, Fiber Distributed Data Interface (FDDI) was also developed (Lehr, 1996).

an agreement on just one of two alternative proposals.¹⁴ ATM instead was a technology originally developed in the fields of telephony with the ambition of becoming viable for the LAN environment.¹⁵ These standards followed divergent trajectories of development and only Fast Ethernet eventually became dominant for high speed communication.

Pre-standard Fast Ethernet products were introduced by Grand Junction at the end of 1993. Early adapter cards for Fast Ethernet were instead shipped in 1994.¹⁶ A significant milestone was the implementation in the hardware of adapter cards of 'auto-negotiation', a mechanism capable of auto-detecting both network speed and configuration of the equipment they were attached to. This mechanism, while letting different segments of an existing network operate at different speeds, enabled network

¹⁴ As high speed upgrades for Ethernet, Fast Ethernet and 100VG-AnyLAN were both proposed to the IEEE in November 1992 by 3Com Corp. and Hewlett Packard respectively. The 3Com proposal, backed by SynOptics, Sun Microsystems and LAN Media Corp., was modelled on the recent work of a start-up company, Grand Junction Networks, which studied the possibility of upgrading the ET speed to 100Mbps while maintaining the CSMA/CD access method adopted by Metcalfe's Ethernet. The solution proposed by Hewlett Packard and supported by AT&T, named 100Base-VoiceGrade (100Base-VG), adopted a so called 'demand priority scheme' as the access method which would enable it to handle two different kinds of traffic by prioritising the access to the network. The adoption of this access method would turn out to be the major shortcoming of this standard relative to Fast Ethernet.

¹⁵ The roots of the development of ATM extended back to early researches in cell switching conducted at the Bell Laboratories in the late 1960s when the system of cell relay, which would constitute the basis of ATM, had been conceived under the name of Asynchronous Time Division Multiplexing (ATDM). Some work on its principles had continued during the 1970s. However, ATDM was never commercialized (Gould, 1994: 122).

¹⁶ Data Communications, (May 21, 1994: 16)

managers to know all the basic information about the features of the adapter, provided that it was implemented in all the equipment within the same segment. Moreover, a further capability of supporting different cabling types (coaxial as well as twisted pair) on the same Ethernet environment was offered by the presence of a media independent interface (MII) mechanism standardized by the IEEE (Melatti, 1994).

Table 1 summarises the milestones in the evolution of Ethernet that have been reviewed in this section together with other main events that have characterised the development of LAN technology.

[Table 1 about here]

4. Data and summary statistics

In this paper, we use US patent data as our main data source. The technological field of LAN technology is delimited using the International Patent Classification (IPC) which seems better suited for our purposes than the classification provided by the US patent office. We consider patents contained in the subclasses of class IPC H04L12 which refers to 'Data Switching Networks'. A recent analysis by Grebel *et al.* (2006) has shown that this class has been the most dynamic within the field of telecommunications, experiencing, especially during the 1990s, the highest rate of growth in terms of patents. To check whether the evolution of LAN standards and technologies is in part responsible for this growth, we perform a detailed analysis of patenting activity within this class.

We proceed as follows. First, we take a general look of the patents for the LAN technology within IPC H04L12. Working at a high level of dis-aggregation (7 digits) and on the basis of expert knowledge, we select 10 subclasses as relevant for studying the evolution of LAN technical systems. Table 2 reports the technological subclasses that we have considered relevant for the domain of LAN technology together with their share in the total number of patents. Patent numbers belonging to these classes have been retrieved using the search engine available on the USPTO website.¹⁷ The source for patent information (granted year, assignee and citations) is the NBER patent data-set (Jaffe and Trajtenberg, 2002). The time span covered by the original data-set is 1963-1999. For this paper we have extended the original NBER data-set adding the patents granted up to period of February 2002. Our final data-set has been constructed by matching the patent numbers in our technological classes of interest with the corresponding number in the extended NBER patent data-set.

[Table 2 about here]

It can be noted that the distribution of patents across subclasses is uneven with four subclasses, (1), (2), (7) and (10), accounting for more than 80% of the total patents in the sample. Figure 2 shows the share in the total number of patents over time for each subclass. For dating the patents, throughout this paper we have considered the year in which the patent was granted. In the figure we have highlighted the shares for the four subclasses mentioned above.

¹⁷ Patents have been retrieved by means of queries in the international patent class (ICL) field.

[Figure 2 about here]

The growth in the shares is particularly evident in the case of H04L12/28 (i.e. subclass (1)) and H04L12/56 (i.e. subclass (10)). Subclass H04L12/413 (i.e. subclass (2)) experiences the highest share between 1983 and 1989 and then we witness a further increase between 1995 and 1998 following the standardization of Ethernet and Fast Ethernet respectively. The share of H04L12/56 (i.e. subclass (10)) is always high and increases considerably from 1987 onwards (particularly during the 1990s). Given the time lag between the application and the granting date, the expansion seems attributable to the arrival of the switch as well as to the implementation of high speed standards in use.

Table 3 below presents the pattern of cross-citations for all the chosen technological classes together with a summary statistics of the number of citations made and received.

[Table 3 about here]

Each cell (i, j) reports the number of times a patent is cited or cites another patent in our sample. Reading the table by column gives us the number of times patents in class j have been cited by patents in class i . Reading the table by row gives us the number of times patents in class i have cited patents in class j . The total number of citations received by each class is reported at the bottom of the table. The last column reports instead the total number of citations made by each class.

The following observations can be made. First, for each class the total by column is generally higher than the total by row thus indicating that the chosen technological classes generally tend to be 'recipients' rather than 'sources' of citations. Important exceptions are represented by columns (1), (7) and (10) which, however, correspond to classes with relatively higher number of patents. Second, cells along the main diagonal (i.e. those highlighted in bold), generally contain relatively high values when compared to the rest. This indicates that patents of each class tend to cite and be cited mostly by patents of the same class. This suggests that the chosen classes represent relatively autonomous and coherent technological fields in our selection of the LAN technology domain.

It is worth remarking the peculiarities of patents in class (10). Around 78% of citations received by this class are self citations. Moreover, patents in this class cite a relatively high number of patents belonging to other technological classes. Class (10) contains patents relative to packet switching which is a relative broad definition encompassing a variety of functions and aspects of network technology, ranging from the definition and design of equipment to the implementation of specific standards in use. It is understandable that many patents in this class refer to other more specific classes. An example is provided by class (2) which tends to received most citations by classes (6) and (10). Class (2) contains patents referring to the Ethernet standard while classes (6) and (10) patents that can be broadly related to the design of LAN equipment such as hubs and switches respectively. Being LANs technical systems, most of these citations refer to the implementation of the standard specifications in use.

To check whether the results of Table 3 are affected by the relative differences in the total number of patents in each chosen class, we normalize the observations by the number of total patents in the chosen class. In particular, we construct the following symmetric Relative Specialization Index (RSI):

The RSI is given by:

$$RSI = \frac{AI - 1}{AI + 1} \quad (1),$$

where AI is the Activity Index calculated as the share of the citations received by each technological class and the share of the technological class in the total number of citations (received and made). It takes values in the $[-1, 1]$ range.

$$AI = \frac{\frac{c_{ij}}{\sum_i c_{ij}}}{\frac{\sum_j c_{ij}}{\sum_i \sum_j c_{ij}}} \quad (2),$$

where i refers to citations received, j refers to citations made and c_{ij} to the number of citations. Since the RSI is the ratio of AI minus 1 and AI plus 1, it indicates whether a class has a higher than average citation activity in a given class ($RSI > 0$), or a lower than average ($RSI < 0$).

Table 4 below reports the RSI for each technological class.

[Table 4 about here]

Again numbers on the main diagonal refers to self citations and are always positive thus signalling a higher than average intra-class citation activity. This result confirms the previous results in Table 4. Focusing on specific subclasses, some interesting patterns emerge. First, subclass (2), which contains Ethernet related patents, receives a higher than average number of citations from subclasses (3) and (6) that refer to Token Ring and LAN equipment related patents respectively. As already mentioned above, most of the citations from class (6) constitute attempts of implementing the standards specification in hubs. Second, some subclasses tend to 'specialize' in the sense that they tend to be either sources or recipients of citations. Cells in row (6) for instance are almost always positive indicating that this subclass tends to cite more than average the other classes. Off diagonal cells for subclass (10) are instead always negative indicating that this subclasses displays a lower than average citation activity both as a recipient and as a 'source' of citations. Finally, it is interesting to note how some subclasses display a similar pattern of citations. This is particularly true for subclasses (8) and (9) which, as we saw in Table 3 display a low number of citations and tend, on average, to make and receive citations from the same subclasses. Interestingly, patents in these subclasses refer to 'circuit switching' a type of technology linked to telephony and not so widespread in the realm of data communications

5. Patent citations networks in the LAN technology

In this section we focus on the structure of connectivity of a specific subclass in our overall patent population for LAN technology. We perform the structure of connectivity study on subclass H04L12/413 which pertains to the Ethernet standard. Figure 3 below provides a general overview of the development of the network of patent citations for this specific subclass over time. There are six series in the graph: 'PATENTS' represents the cumulative number of patents of this subclass, the series 'START' refers to the number of start points, the series 'END' represents the number of endpoints, and the series 'ISOLATES', 'INTERMEDIATES' and 'COMPONENTS' represent the number of isolate vertices, the number of intermediate points and the number of network components¹⁸ respectively. The number of patents and components are measured on the vertical left and right axes respectively.

[Figure 3 about here]

Two observations are in order. First, the figure suggests that all the series display a behaviour characterized by an almost constant growth rate which tends to slightly level off toward the end of the time period. Our interpretation is that the pattern reflects a sustained phase of exploration of the space of technological opportunities, corresponding to the subsequent generations of the standard (i.e. Ethernet, and Fast Ethernet) which constantly fuel technical progress in this subclass. Second, the number of components is also increasing. This trend would suggest that the development of the

¹⁸ A component is a set of patents which are all connected. For a more detailed discussion, see Wasserman and Faust (1994: 109-110).

technology occurred in different environments without displaying a strong tendency towards technological convergence (if this was the case, we would instead see a decrease in the number of components, as they become increasingly interconnected). However, the overall network, since the early 1990s, is characterized by the presence of a largest component which contains the majority of patents and by a number of smaller components and isolated nodes. In 2002 the largest component, which is displayed in Figure 4 below, contains 456 patents out of a total of 581. This corroborates the idea of considering the patents in the Ethernet class as belonging to one integrated 'technical system'.

[Figure 4 about here]

As a next step in our analysis, we calculate the value of SPNP for each citation pair of the complete network spanning from 1977 to 2002.¹⁹ Then, following the procedure proposed by Hummon and Doreian (1989), we construct the network of main paths as explained in Section 2.

[Figure 5 about here]

Figure 5 displays the largest component of this network of main paths. This network is characterized by a rather long sequence of patents contained in the bottom right part of the graph. The sequence from patent 4096355 to patents 6078591, 6118787 and 6178177

¹⁹ Similar results were obtained using SPLC as measure of the connectivity of the links.

represents what we may call the 'top path' of the network (i.e. the sequence of patent citations going from a start point to an end point whose sum of SPNP values is highest). The second highest 'top path' is represented by the sequence from patent 4332027 to again patents 6078591, 6118787 and 6178177. The third highest 'top path' contains the sequence from patent 4063220 to again patents 6078591, 6118787 and 6178177. These paths are represented in Figure 5 with white dots. Intuitively, these sequences of patents should represent the fundamental flow of knowledge (technological trajectory) in the overall network of patent citations. These patents are listed in more details in Table 5 below. The following points merits attention.

[Table 5 about here]

First and foremost, the path actually originates from two rather distinct trajectories. Patent (No. 4063220) lies at the origin of the first trajectory. This is the patent by Metcalfe *et al.* which set the basis of the CSMA/CD contention mechanism that characterises the functioning of Ethernet. The next three patents on this trajectory (No. 4271523, 4345250, and 4560984) represent attempts at dealing with the design of the collision mechanism within a *baseband network environment*. The second and the third trajectories originate instead from Patent (No. 4096355) and Patent (No. 4332027) respectively and continue through three subsequent patents (No. 4210780, 4412326, and 4598285), all concerned with the design of a collision detection mechanism in *broadband network environments*.²⁰

²⁰ A baseband network, such as Ethernet, is one that provides a single channel for communications across the physical medium (e.g., cable), so only one device can transmit at a time. With broadband, the physical cabling is virtually divided into several different channels, each with its own unique frequency. These

The two trajectories converge on patent (No. 4751701) owned by Hughes Network Systems. This patent deals with the design of a transmission protocol for a new detection mechanism based on detection of a signal gap in the transmission medium following collision rather than detection the presence of simultaneous energy emission from transmitters. Interestingly, this is a solution that seems to allow solving contention problems in both baseband and broadband network environments and justifies the position of this patent at the 'junction' of the two trajectories. All these patents, both baseband and broadband related, are quite specific and revolve around the particular issue of the design of the contention mechanism, and are relatively old. The next six patents (No. 5012467, 5355375, 5568469, 5642360, 5838688, and 6078591) are less specific and more recent. They deal with various aspects of the implementation of the standard in use related to the design of various components of the LAN technical system such as repeaters and hubs, as well as the performance of these types of equipment. The nature of these patents will be analysed in more details below.

Second, patents belong to companies that have been the major players in the birth and growth of the networking industry such as Xerox, IBM, and 3Com. However, as it can be seen from Table 6, no company seems to be 'dominant' in the sense of claiming ownership of the majority of the patents that lies on this fundamental path that we have identified in the network. This suggests that no company seems to be strategically placed along the main path of knowledge flow and that the clustering reflects a general

different frequencies are transferred onto the network cabling in such a way to allow multiple simultaneous transmissions to occur (see <http://www.ethermanage.com/ethernet/enet-faqs/ethernet-faq.html>, accessed January 22, 2008).

engineering logic rather than the logic based on the internal search strategy of a specific company. This is also consistent with the literature which has emphasized how Ethernet achieved market dominance as a consequence of licensing and the pursuit of an open standard strategy (von Burg, 2001).

Finally, a comment on the relationship between the number of citations and the positioning of the patent along the top path is in order. Table 6 below reports the list of the 20 most cited patents in the Ethernet subclass.

[Table 6 about here]

While some of the patents that lie on the path we have identified are also included in the list of the most cited patents, it can be noted that some important patents, such as patent No. 4751701 discussed above, do receive a relatively lower number of citations and/or are not included in the list. In this case, it would seem that the Hummon and Doreian method has allowed the identification of a patent that, although not characterized by many citations, is positioned at a strategic 'junction' along the trajectory of technological development, linking the broadband and baseband environment. However, a qualification is in order. There are other patents, or groups of patents, that in the underlying network of citations represented in Figure 4 could serve the same critical role.²¹ However, if one accepts that the Hummon and Doreian approach is able to pin down the most significant knowledge flows, it is likely that such alternative patents or

²¹ We thank an anonymous referee for raising this point and stimulating us to explore more thoroughly this issue.

group of patents will not embody the extent of knowledge flows passing through patent 4751701. The reason for this is that the knowledge flow measure we use does not only depend on direct citations, but especially on indirect citation links.²²

Values of SPNP can also be calculated for any time interval. Thus, by repeating this exercise for a sequence of time periods, it is possible to produce a series of 'snapshots' that portrays the evolution of the patent citation network of Ethernet over time. This exercise provides an interesting perspective on the dynamics of innovation in large technical systems. We may expect that when inventive activities are guided by a consolidated search heuristics patents of the temporal sequence of top paths will show a high degree of overlap. Vice versa, in phases of high uncertainty we could expect a relatively heterogeneous temporal sequence of paths. The evolution of the top path over time is portrayed in the sequence of Figure 6. Note that the years in the figure indicate the intervals for which the networks were calculated rather than the years in which the patents were granted.

[Figure 6 about here]

²² To probe further into the role of patent 4751701 we have removed this patent and its citation links from the sample and calculated again the network of main path. It is interesting to note that, in this counterfactual world, the top path of the network includes almost the same sequence of patents as found in Figure 5. In other words, in the counterfactual world, we see a similar dynamics of convergence between the broadband and baseband environments. However, this converge occurs much later in time, with patent 5568469 (filed in 1994), instead than with patent 4751701 (filed in 1985). Thus, in our judgment, it is not far-fetched to ascribe to patent 4751701 a significant role in bridging the two environments *at that point in time*.

In our case, we observe that the patents at the origins of the two trajectories of the top path of Figure 5 appear relatively early in our sequence of ‘snapshots’. However, the baseband trajectory seemed to evolve relatively more rapidly than the broadband trajectory which took off only between 1984 and 1988. It is also interesting to notice that in the later stage of development, the two trajectories first converge and then branch off into separated technological environments. This evolution is clearly represented in the last snapshot of our sequence (1977 – 2003), in which nodes of different shapes and colours identify patents belonging to distinct environments. In the remaining part of this section we analyse the specificities of these clusters and the extent that they are related to particular events in the development of the Ethernet standard.

Red circles identify the first technological environment characterised by 17 patents. These patents are similar in the sense that they all are variations on the Metcalfe *et al.* 1977 patent (No. 4063220). However, each sequence is characterised by a specific research heuristic. Baseband related patents lie in the lower sequence (starting from patent No. 4063220). Each of them embodies some sort of modification of the mechanism aimed at improving collision detection.²³ Broadband related patents instead lie in the

²³ Within information communication systems there are several methods for detecting and avoiding collision. One method consists in detecting when a collision occurs by measuring phase shift in the information signal received. This is the method embodied in the original 1977 Ethernet patent by Metcalfe and Boggs. Patent No. 4271523 represents an improvement upon this patent in the sense of reducing the susceptibility of phase shift to noise in the medium. Patents No. 4345250 and No. 4376278 instead, both owned by Honeywell, aim at reducing collision of signals by charging the medium with a predetermined

upper sequence (starting from patent No. 4096355). While all these patents generally seek to improve the collision detection mechanism as in the baseband case, this improvement is mainly seen as instrumental to the achievement of a higher transmission bit rate in principle obtainable in broadband networks. Patent (No. 4751701), owned by Hughes Networks and discussed above, is the prelude to the convergence of the two sequences. Interestingly, convergence occurs right after 1985 the year in which the IEEE officially complete the standardisation of Ethernet, as if the official ratification reduced uncertainty and put an end to a phase of exploration of different alternatives.

The main core of the network branches off in four different environments. The environment identified by white circles was the earliest to develop. Innovations belonging to this cluster aim at simplifying the design of the network and at improving reliability of transmission. Simplification was mainly sought through the reduction of the number of transceivers along the network (see patents No. 472172 and No. 4568930 for instance). Reliability instead tended to be achieved by improving cabling redundancy as in patents (No. 4658396 and No. 4780869). Black squares characterise a second cluster of four patents. These patents pertained mainly to applicability of contention detection mechanisms to context characterised by a plurality of buses. The cluster does not grow after 1991 and it does not seem to be particularly important for our analysis of the subsequence evolution of the Ethernet trajectory.

voltage. Another method consists in using a time slot to check when transmission may occur. Other methods entail the use of a controller to ask devices permission to transmit or the division of the channel into different frequencies with different bandwidth (Hopkins, 1979).

The two environments that branched off from patent No. 5012467 deserve instead to be analysed in more detail since they both mark a discontinuity with the early phase of exploration. While early improvements were mainly characterised by variation upon 'the collision detection mechanism theme', these two clusters are mainly related to applications of Ethernet in use and therefore to the design of the components of the overall technical system. The cluster of yellow diamonds refers to equipment such as transceivers and hubs that needed to be deployed in LANs as they started growing in extension. The patent that lies at the origin of this cluster is a design of a method and apparatus for collision detection in a transceiver (patent No. 5012467). This patent is owned by 3Com and the inventor is Ronald Crane who, as we saw in Section 3.1, in 1981 adapted the early transceiver designed by Metcalfe and Boggs to shrink equipment size and open for Ethernet the way to the PC market. Other patents include the design of a controller for hub (patent No. 5355375) and a method for improving hub performance by reducing latency and jitter (patent No. 5568469).²⁴ The rest of the patents within this cluster deal with data packets control flow. Interestingly, five out of seven of these patents are owned by Advanced Micro Devices. This evidence suggests that in the most recent years the search trajectory has become more and more company specific and moved away from the 'open' engineering logic that seemed to prevail in the early years of the evolution of the trajectory. Moreover, this is also consistent with recent developments in which semiconductor companies are increasingly challenging

²⁴ Latency is the delay existing from the source sending a packet to the destination receiving it. Jitter instead is an abrupt and unwanted variation of one or more signal characteristics (i.e. the interval between successive pulses), the amplitude of successive cycles, or the frequency or phase of successive cycles of transmission.

traditional networking companies as innovators, a pattern that may lead to a change in leadership in the LAN industry.

Similar considerations apply to the cluster indicated by the light blue triangles at the bottom left hand side of Figure 6. All these patents refer to the Full Duplex implementation of Ethernet which occurred in parallel with the arrival of switching. In Full Duplex, data can be sent and received simultaneously over a link. One advantage of this approach is that the full duplex link can theoretically provide twice the bandwidth of normal (half duplex) Ethernet. The full duplex mode of operation requires that each end of the link only connects to a single device, such as a workstation or a switched hub port. This represented a paradigmatic change with respect to the functioning of early networks based on the principle that the total bandwidth available for communication had to be shared among all the users (Spurgeon, 2000). Within this cluster, an important sequence is constituted by the three patents (No. 5311114, 5504738, and 5648956). All these patents pertain to the design of a transceiver for Ethernet 10Base-T and are owned by Seeq Technology Inc., a spin-off from Intel, which was among the first companies to develop a Full Duplex version of Ethernet and to market it in 1993 through its subsidiary Kodiak Technology. The other patents in this cluster are ancillary to this main trajectory. Indeed, the implementation of Full Duplex features required further changes in the hardware specification of the equipment such as the development of 'auto-negotiation' which the capability of switching from half duplex to full duplex (patent No. 5432775) or between different media specification of Ethernet (patent No. 5444856). Also these patents, are owned by traditional semiconductor firms (Advanced Micro Devices and

Intel respectively) somewhat confirming the increasing importance of these companies in the LAN industry.

6. Concluding remarks

This paper has analysed the pattern of technical change in large technical systems as captured by patent citations networks. Given the characteristics of innovation in these systems (i.e. distributed inventive activity, 'systemic nature', cumulativeness, and speed of technical change), we have argued that it is possible to use measures of structure of connectivity such as the *search path link count* or the *search path node pair* proposed by Hummon and Doreian (1989) to identify the major paths and the critical bottlenecks characterizing the evolution of the system.

In the case of Ethernet, our approach was capable of producing fruitful insights. The main trajectories we have identified using patent citation networks displayed a coherent economic and engineering logic, consistent with more qualitative accounts of the evolution of the Ethernet standard. Indeed, our evidence has revealed that inventive activity occurred in distinct phases. During the first phase, the main focus was on the definition of the standard and inventors were mainly concerned with changing and refining the original contention mechanism proposed by Metcalfe and Boggs. This 'exploration' phase ended with the official standardisation of Ethernet that consolidated the specifications of the technology. The second phase was concerned with the application of Ethernet in use and the design of the components of the overall technical system (i.e. transceivers first, then hubs and switches). The third phase dealt with further improving the functioning of the individual components.

Further, the approach sheds light on critical patents which have punctuated the development of this technology. In the literature, the assessment of the economic and technological significance of individual innovations is based on the weighting of individual patents by means of received citations. The approach based on the structure of connectivity analysis opens another interesting perspective. As noted, some of the key patents calculated using citations as weights do appear in our mapped trajectories. This suggests that the approach used in this paper could fruitfully integrate the simple counting of citations. However, the study of the structure of connectivity of patent citation networks can also provide additional insights, by pointing out patents with low number of citations that occupy strategic positions in the unfolding of a technological trajectory.²⁵

Although the method is of relatively straightforward implementation we would like to suggest some word of caution. First, it is necessary to adopt particular care in the identification of the relevant patent classes that constitute the object of analysis. Our position is that, given the uncertainty surrounding the criteria adopted by patent offices for assigning a patent to a specific class, when performing this type of exercises is important to try out different possible selections of the patent universe considered and devote some effort to investigate the patterns of citations existing between the various patent classes. Finally, as already noted by Verspagen (2007), the tools for the analysis of the structure of connectivity should not be employed 'blindly' but in combination with a detailed study of the engineering trends in the field in question.

²⁵ In this respect, of course, it is necessary to take into account the qualifications made above in Section 5.

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LIST OF FIGURES

FIGURE 1A: CALCULATION OF SPLC AND SPNP

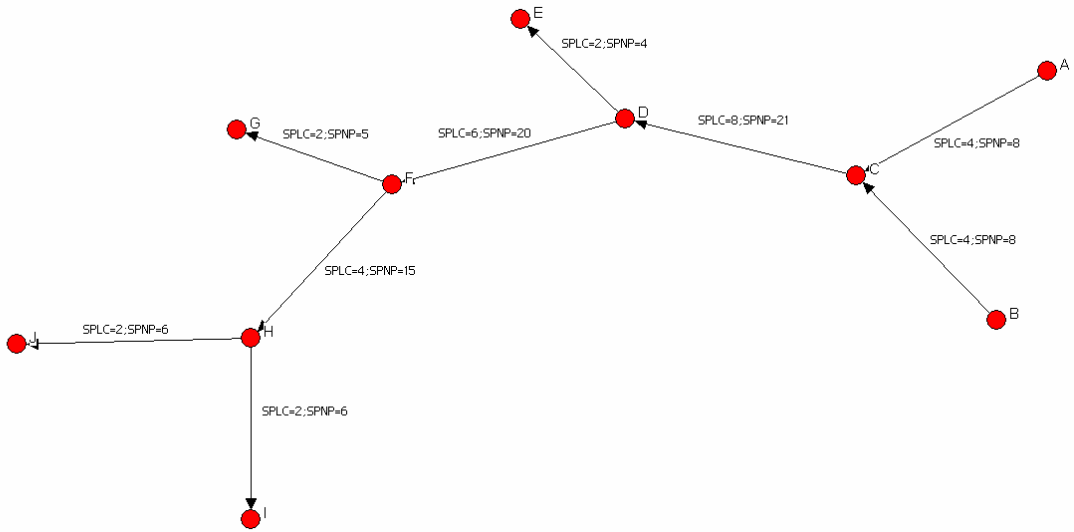


FIGURE 1B: THE NETWORK OF MAIN PATHS

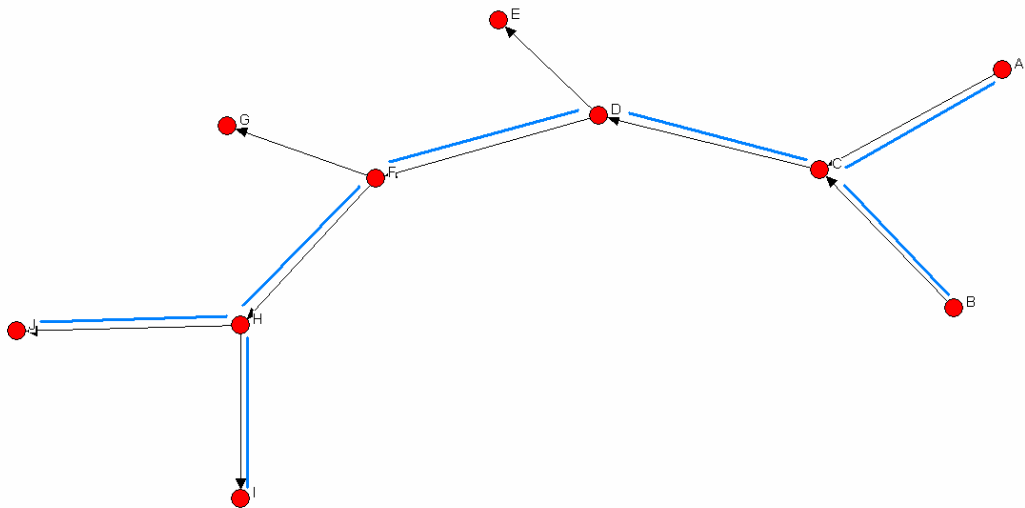


FIGURE 2: SHARE OF LAN PATENTS FOR SELECTED SUBCLASSES

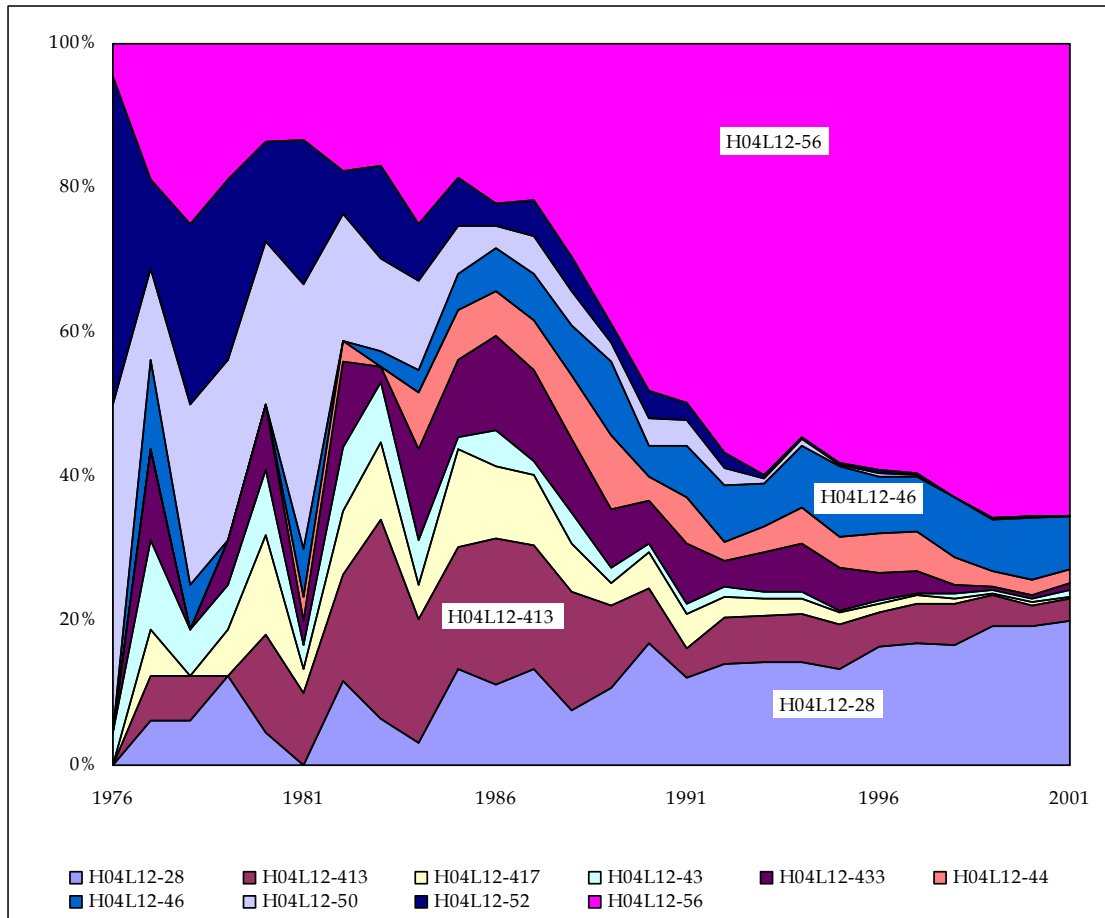


FIGURE 3. DEVELOPMENT OF THE ETHERNET PATENT CITATION NETWORK OVER TIME

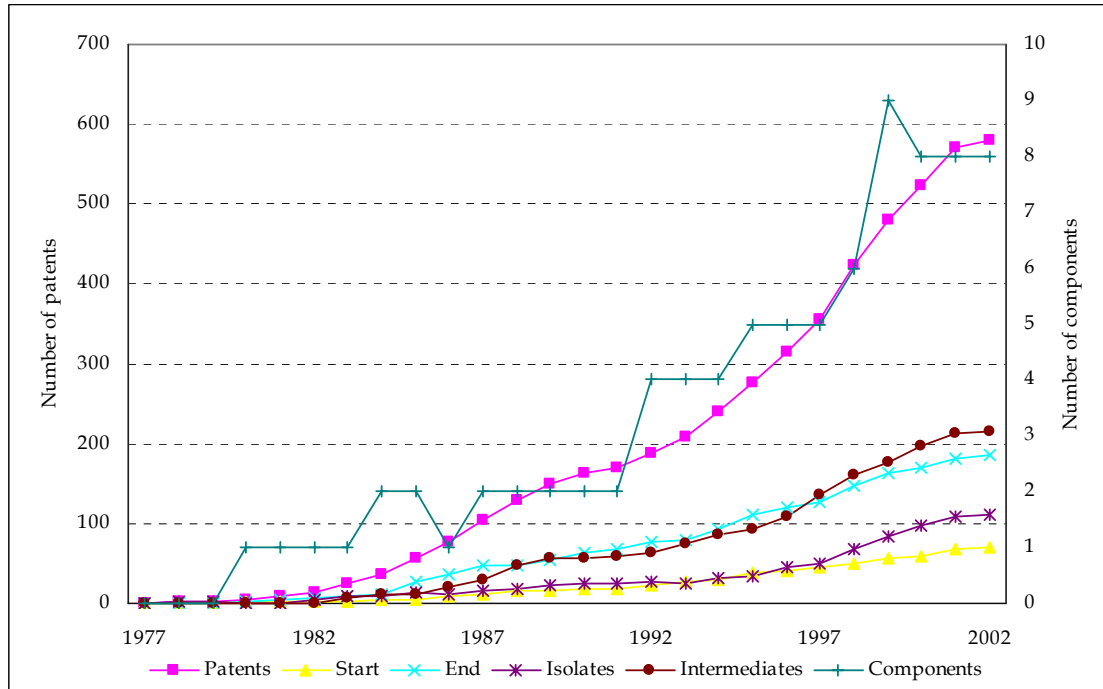
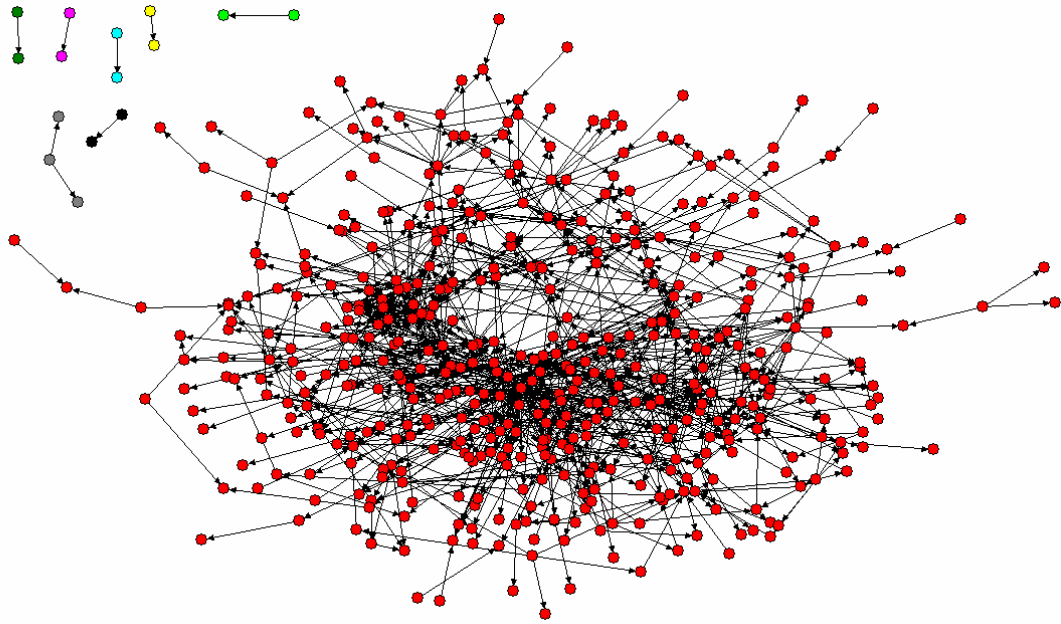


FIGURE 4. PATENT CITATION NETWORK FOR THE ETHERNET CLASS, 1976-2002



Isolates have been deleted

FIGURE 5. THE LARGEST COMPONENT IN THE NETWORK OF 'MAIN PATHS' FOR THE ETHERNET NETWORK, 1977-2002

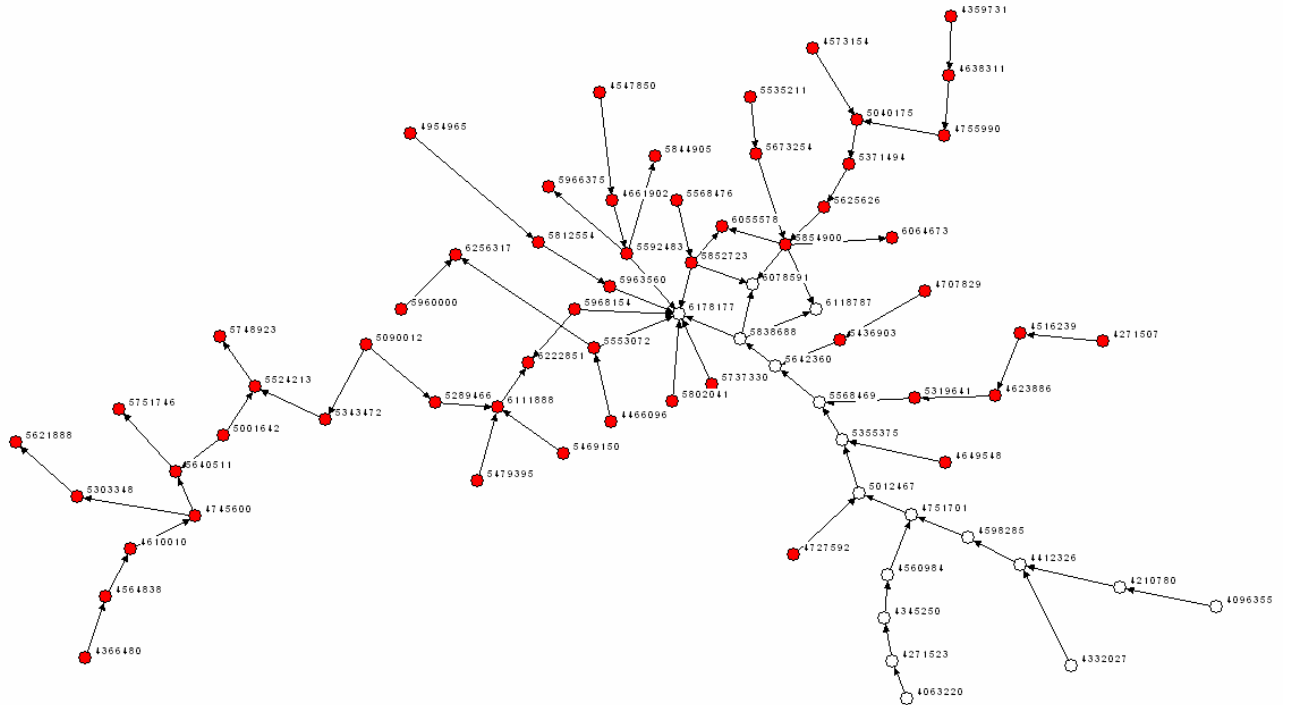
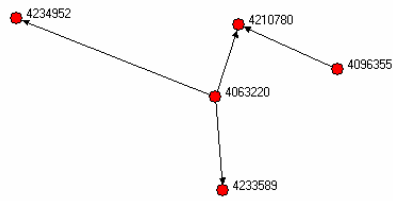
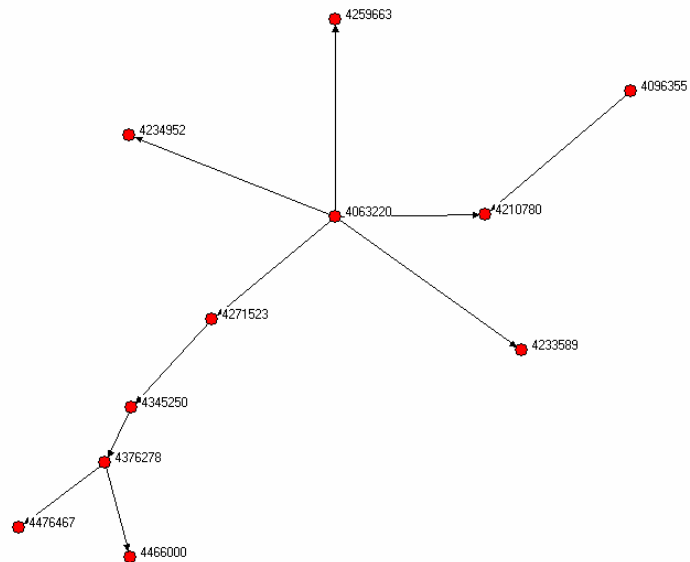


FIGURE 6. THE EVOLUTION OF THE TOP PATH OVER TIME

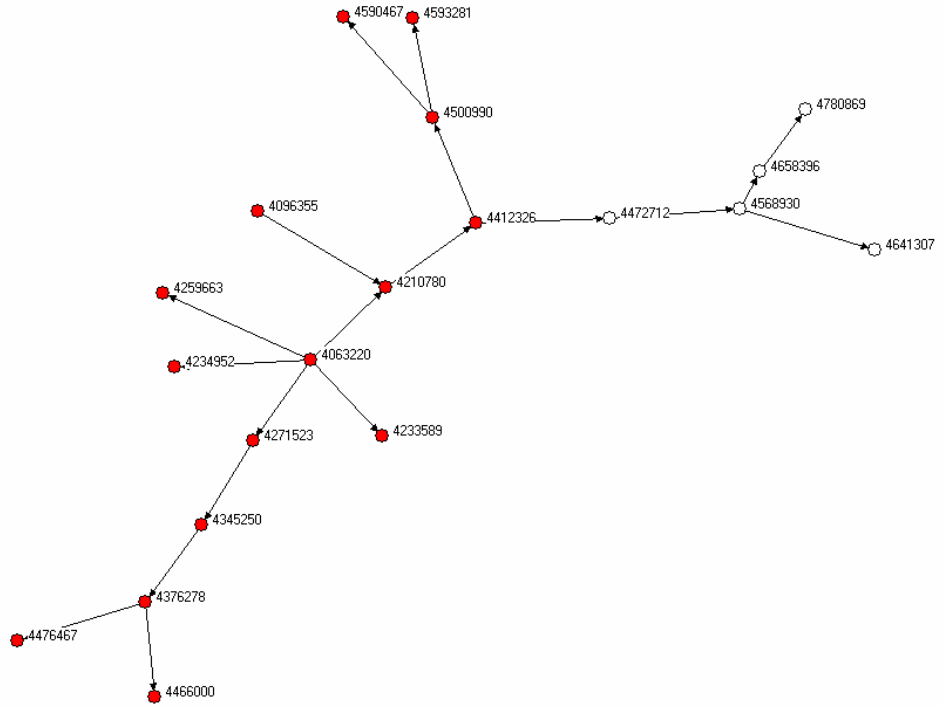
1977-1980



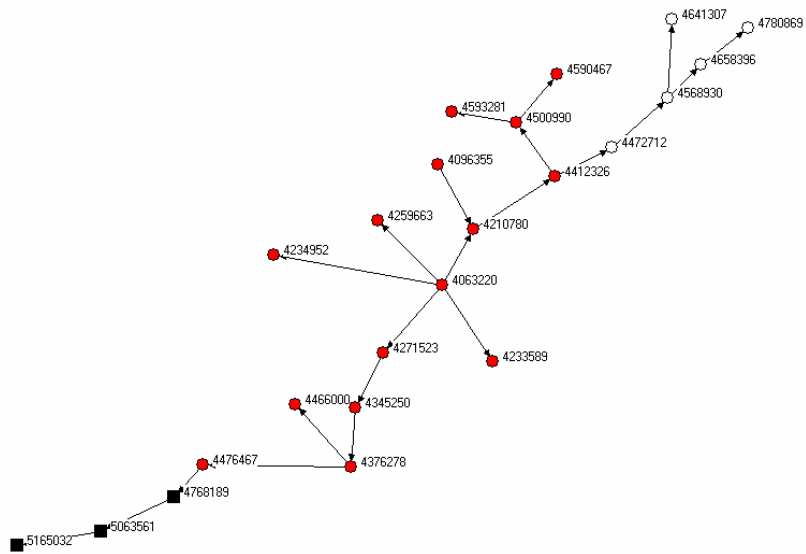
1977-1984



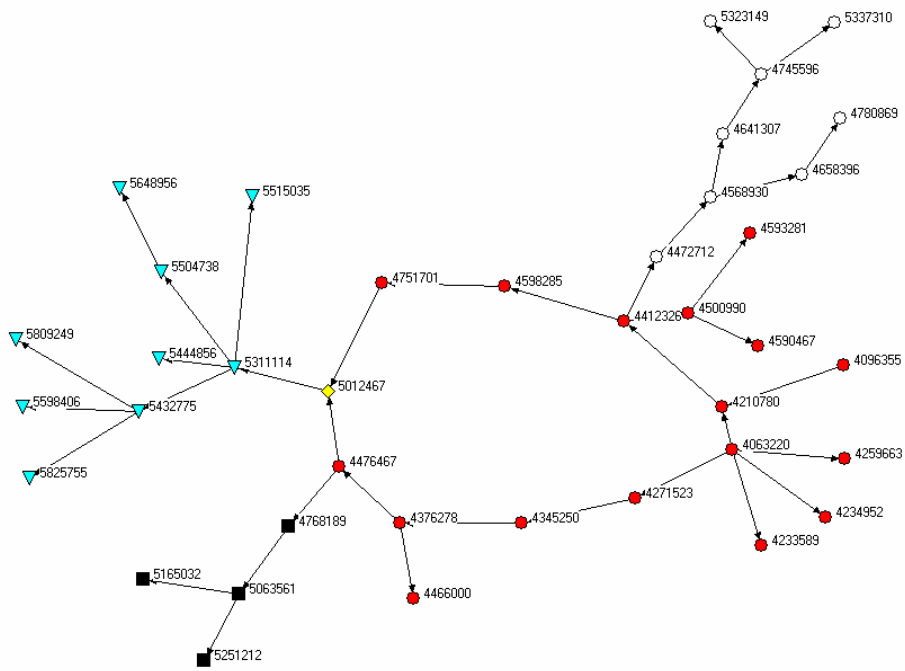
1977-1988



1977-1992



1977-1998



1977-2002

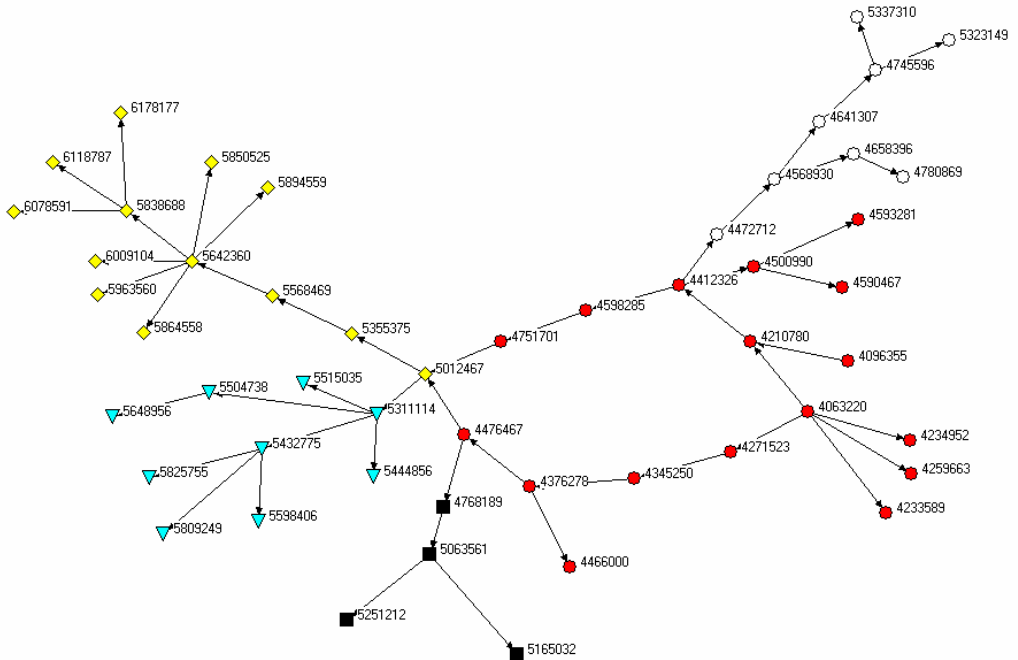


TABLE 1. MILESTONES IN THE EVOLUTION OF LANs

Year	Ethernet	Other LAN standards and equipment
1970-71		Farber's UC Irvine Ring
1973	Experimental Ethernet at Xerox PARC	
1974	Design of early Ethernet transceiver	
1980	Bluebook v. 1 by DIX consortium	Star shaped Token Ring at MIT
1981	Ethernet transceivers and controllers for minicomputers	
1981	Thick Ethernet (10Base-5)	
1981	AMD and Mostek deliver ICs for Ethernet	
1981	3Com delivers Thin Ethernet (10Base-2) and transceivers and boards for PCs	Proteon delivers a proprietary 10Mbps Token Ring
1982	Bluebook v. 2 by DIX consortium	
1985	Ethernet standard ratified (IEEE 802.3)	SynOptics delivers first Multiport Ethernet Hub
1985	Thin Ethernet (10Base2)	
1987	IEEE begins official standardization of 10Base-T	
1988		16Mbps Token Ring for STP wire
1990	10Base-T standard is ratified (IEEE 802.3i)	Kalpana delivers the first Ethernet LAN switch
1991		16Mbps Token Ring for UTP wire
1992	IEEE begins official standardization of Fast Ethernet	
1993	Grand Junction delivers first Fast Ethernet product	
1995	Fast Ethernet standard ratified (IEEE 802.3u)	

TABLE 2. PATENT CLASSES AND DEFINITION

	IPC Patent Class	Definition	No of patents
(1)	H04L12/28	Data switching networks characterized by path configuration (i.e. LAN)	1767
(2)	H04L12/413	Bus networks with random access (i.e. CSMA/CD)	581
(3)	H04L12/417	Bus networks with deterministic access (i.e. token system)	170
(4)	H04L12/43	Loop networks with synchronous transmission (i.e. Time Division Multiplexing method)	101
(5)	H04L12/433	Loop networks with asynchronous transmission (i.e. token ring method)	319
(6)	H04L12/44	Star or tree networks	369
(7)	H04L12/46	Interconnection of networks	809
(8)	H04L12/50	Circuit switching systems	129
(9)	H04L12/52	Circuit switching systems using time division techniques	102
(10)	H04L12/56	Packet switching systems	6137

Source: International Patent Classification, 8th edition, version 2007.1

TABLE 3. CITATIONS MADE AND RECEIVED BY IPC CLASS

IPC Class		Cited										TOT citing (j)
		(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	
Citing	(1)	2723	412	185	71	127	162	596	24	20	2849	7169
	(2)	316	1116	151	20	82	315	189	9	6	466	2670
	(3)	75	124	266	16	121	25	32	2	2	97	760
	(4)	50	9	16	84	66	9	15	13	12	136	410
	(5)	66	51	121	97	580	56	189	9	7	253	1429
	(6)	179	371	47	41	127	552	379	40	38	548	2322
	(7)	579	250	86	81	294	445	3008	42	24	6217	11026
	(8)	5	4	5	4	9	5	9	57	52	63	213
	(9)	4	2	4	3	5	2	3	53	50	24	150
	(10)	2732	854	371	254	886	1200	5259	163	105	37211	49035
TOT cited (i)		6729	3193	1252	671	2297	2771	9679	412	316	47864	75184

(1): H04L12/28; (2): H04L12/413; (3): H04L12/417; (4):H04L12/43; (5): H04L12/433; (6): H04L12/44; (7):H04L12/46; (8): H04L12/50; (9): H04L12/52; (10): H04L12/56

TABLE 4. RELATIVE SPECIALISATION INDEX

IPC Class		Cited									
		(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)
Citing	(1)	0.619	0.150	0.216	0.052	-0.266	-0.240	-0.215	-0.242	-0.202	-0.231
	(2)	0.139	0.816	0.545	-0.087	0.003	0.524	-0.290	-0.238	-0.303	-0.570
	(3)	0.049	0.587	0.909	0.405	0.678	-0.057	-0.507	-0.351	-0.230	-0.666
	(4)	0.153	-0.319	0.402	0.917	0.681	-0.253	-0.557	0.705	0.749	-0.315
	(5)	-0.319	-0.087	0.671	0.768	0.860	0.031	0.013	0.069	0.076	-0.565
	(6)	-0.075	0.580	0.097	0.329	0.283	0.732	0.118	0.517	0.591	-0.459
	(7)	-0.260	-0.304	-0.362	-0.097	-0.068	0.045	0.359	-0.180	-0.318	-0.061
	(8)	-0.584	-0.387	0.170	0.356	0.161	-0.222	-0.506	0.960	0.966	-0.366
	(9)	-0.541	-0.522	0.231	0.383	0.044	-0.469	-0.731	0.969	0.975	-0.598
	(10)	-0.233	-0.418	-0.375	-0.265	-0.257	-0.202	-0.091	-0.245	-0.325	0.088

(1): H04L12/28; (2): H04L12/413; (3): H04L12/417; (4):H04L12/43; (5): H04L12/433; (6): H04L12/44; (7):H04L12/46; (8): H04L12/50; (9): H04L12/52; (10): H04L12/56

TABLE 5. THE LONGEST SEQUENCE OF PATENTS ON THE MAIN PATH

Pat. No.	App. Year	Assignee	Patent content
4063220	1975	XEROX Corp.	Multipoint data communication system with collision detection
4096355	1977	IBM	Common channel access method for a plurality of data stations in a data transmission system and circuit for implementing the method
4210780	1978	The Mitre Corp.	Multiple access digital communications system
4271523	1979	Ford Motor Company	Contention interference detection in data communication receiver
4332027	1980	Burroughs Corp.	Local area contention network data communication system
4345250	1980	Honeywell Information Sys.	Information communication system with collision avoidance
4412326	1981	Bell Telephone Labs	Collision avoiding system, apparatus and protocol for a multiple access digital communications system including variable length packets
4598285	1983	AT&T Bell Labs	Scheme for reducing transmission delay following collision of transmissions in communication networks
4560984	1984	Codenoll Technology Corp.	Method and apparatus for detecting the collision of data packets
4751701	1985	Hughes Network Systems	TDM collision detector
5012467	1989	3Com Corp.	Method and apparatus for collision detection in a local area network transceiver
5355375	1993	Network Systems Corp.	Hub controller for providing deterministic access to CSMA local area network
5568469	1994	3Com Corp.	Method and apparatus for controlling latency and jitter in a local area network which uses a CSMA/CD protocol
5642360	1995		System and method for improving network performance through inter frame spacing adaptation
5838688	1996	Advanced Micro Devices, Inc.	Determining the number of active nodes on an Ethernet network by counting a number of packet receptions
6078591	1997	Advanced Micro Devices, Inc.	Apparatus and method for selectively modifying collision delay intervals
6118787	1997	Advanced Micro Devices, Inc.	Apparatus and method for regulating assigned bandwidth in high speed packet switched networks
6178177	1997	Alcatel	Data-processing network having non-deterministic access, but having deterministic access time

TABLE 6. TOP 20 ETHERNET PATENTS BY INTERNAL CITATIONS

Patent No.	Internal citations	Total citations
4063220	74	131
4332027	25	54
5319641	21	29
4412326	19	38
5418784	19	24
5353287	18	25
5436903	18	23
5526355	16	21
4715031	15	20
4210780	14	36
4234952	14	26
5012467	14	22
5121382	14	20
4429384	13	14
4598285	12	18
4623886	12	22
4751701	12	18
5249183	12	23
5642360	12	14
5446735	11	24

Note: Internal citations refer to citations within class H04L12/413 (Ethernet). Total citations refer to the total number of citations within the ten patent classes covering Data Communication Equipment. Patents marked in bold belong to the top path sequence identified in Figure 5.